

Fig. 1
(Prior Art)

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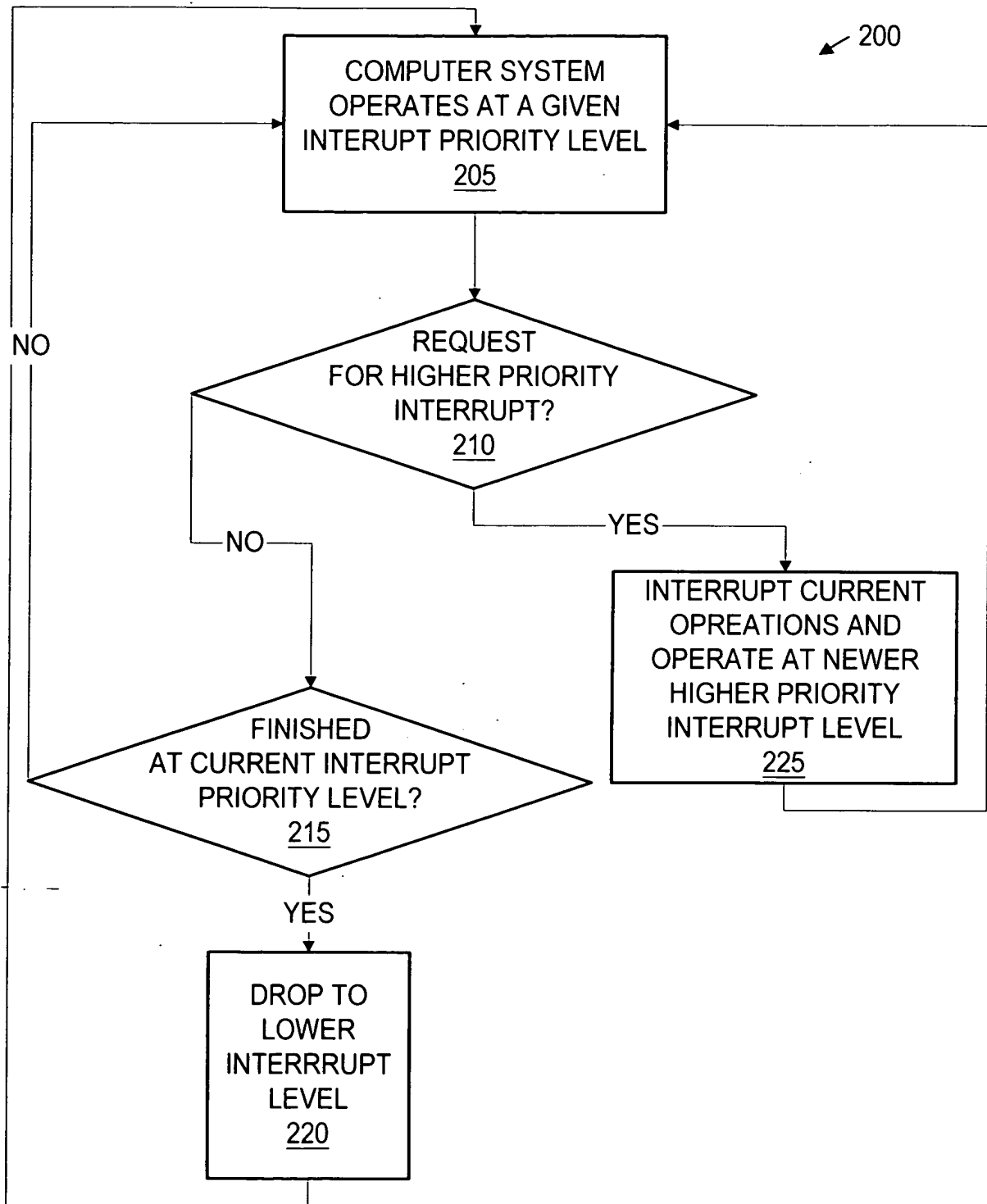


Fig. 2
(Prior Art)

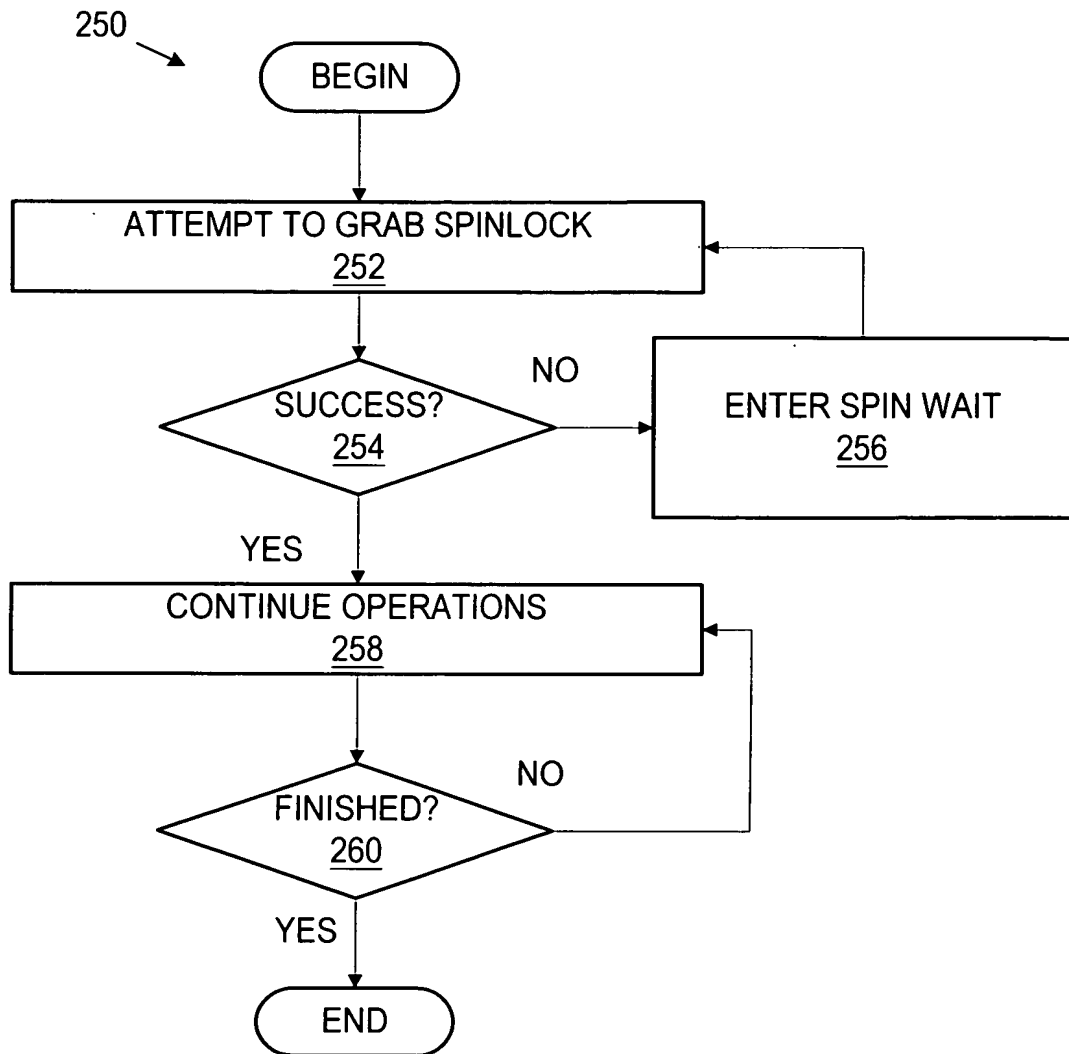


Fig. 3
(Prior Art)

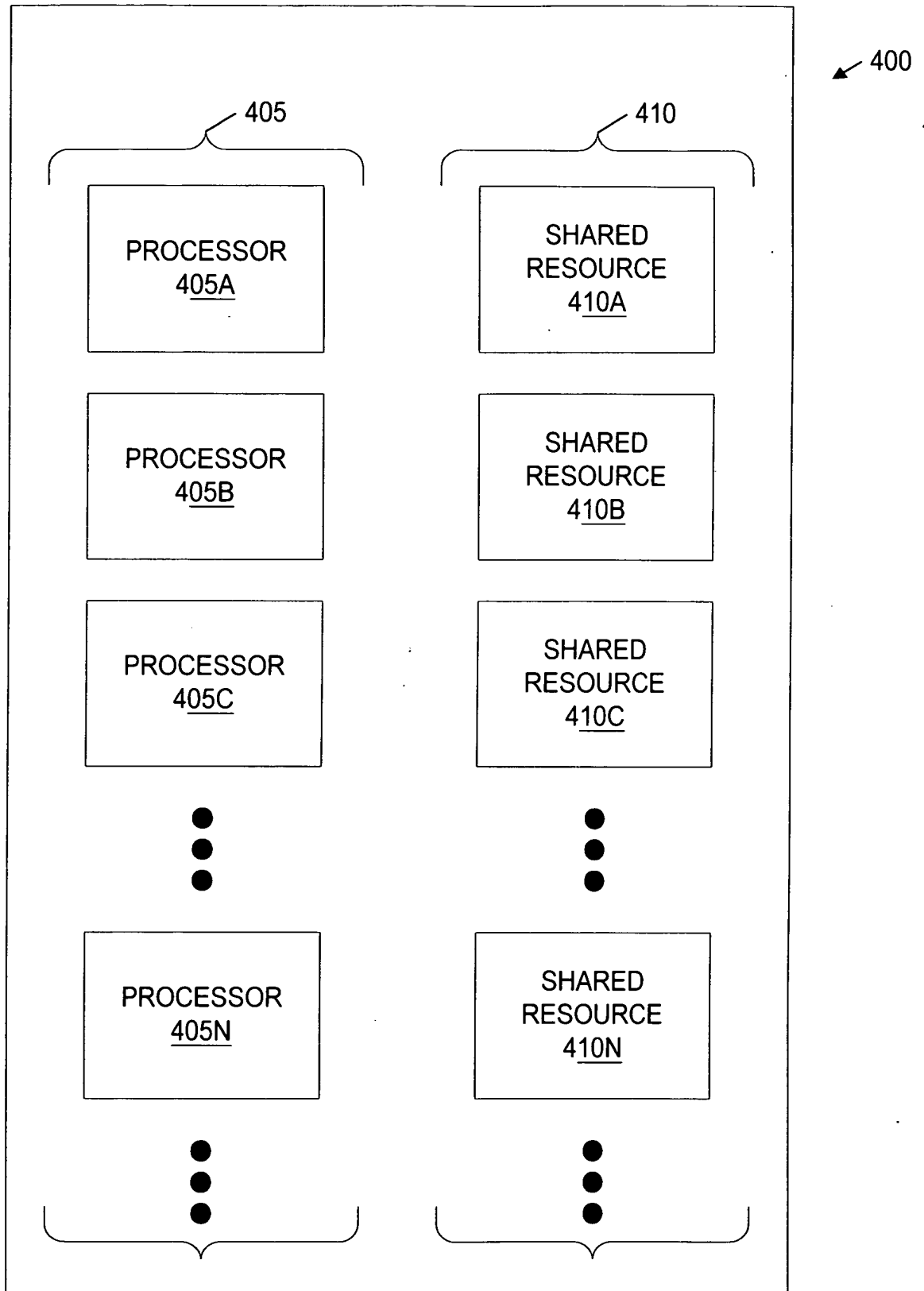
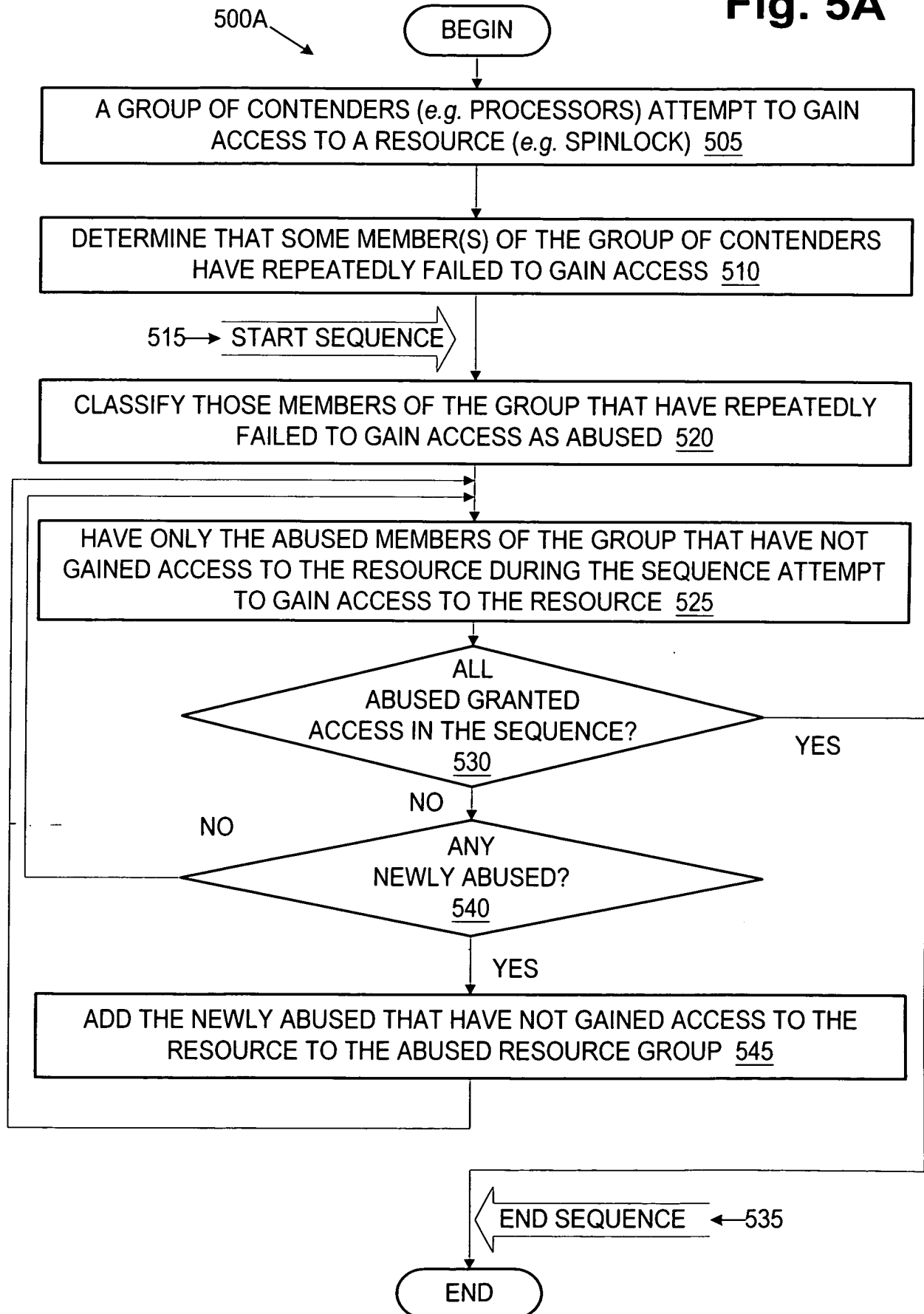


Fig. 4

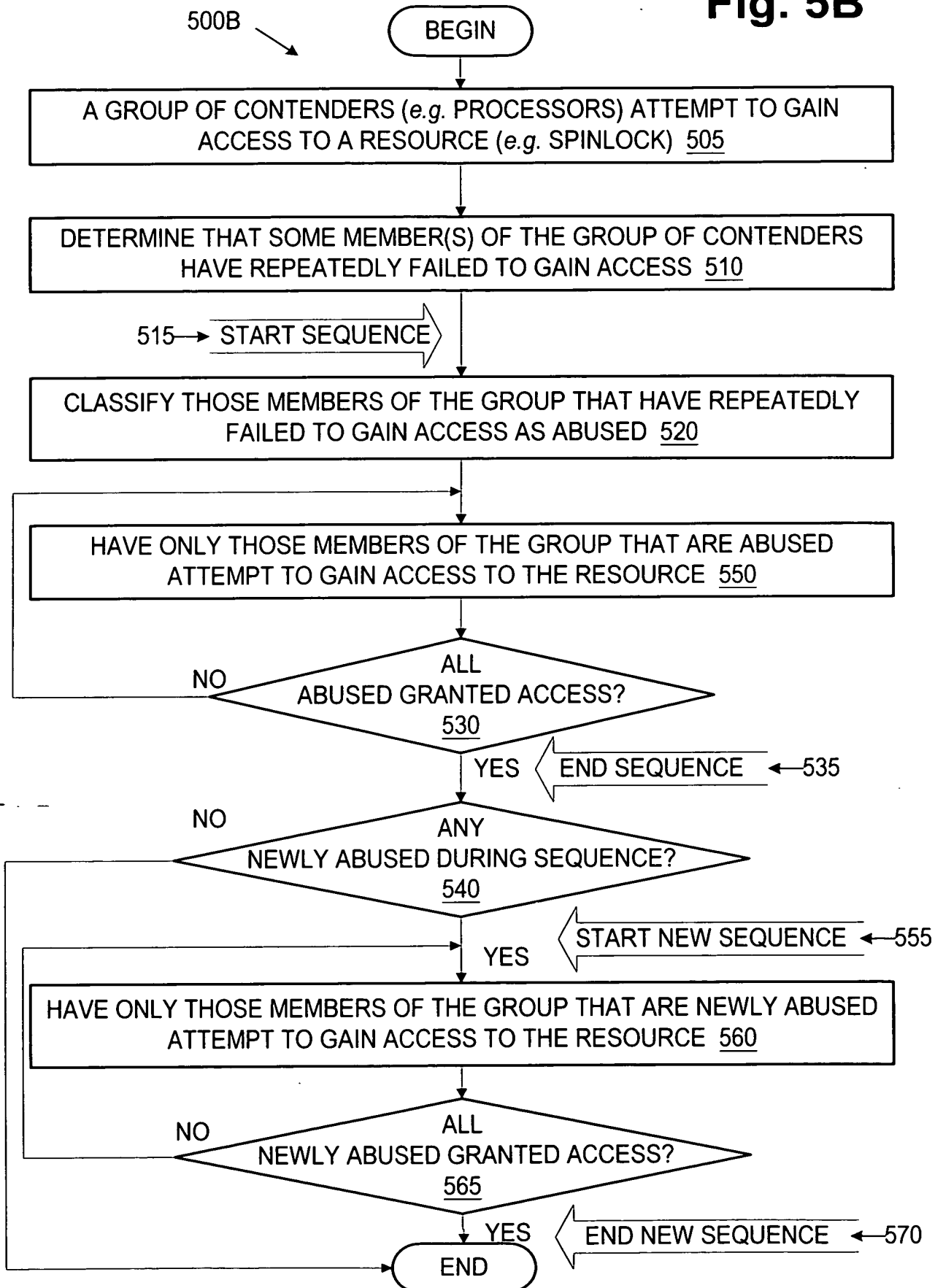
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Fig. 5A



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Fig. 5B



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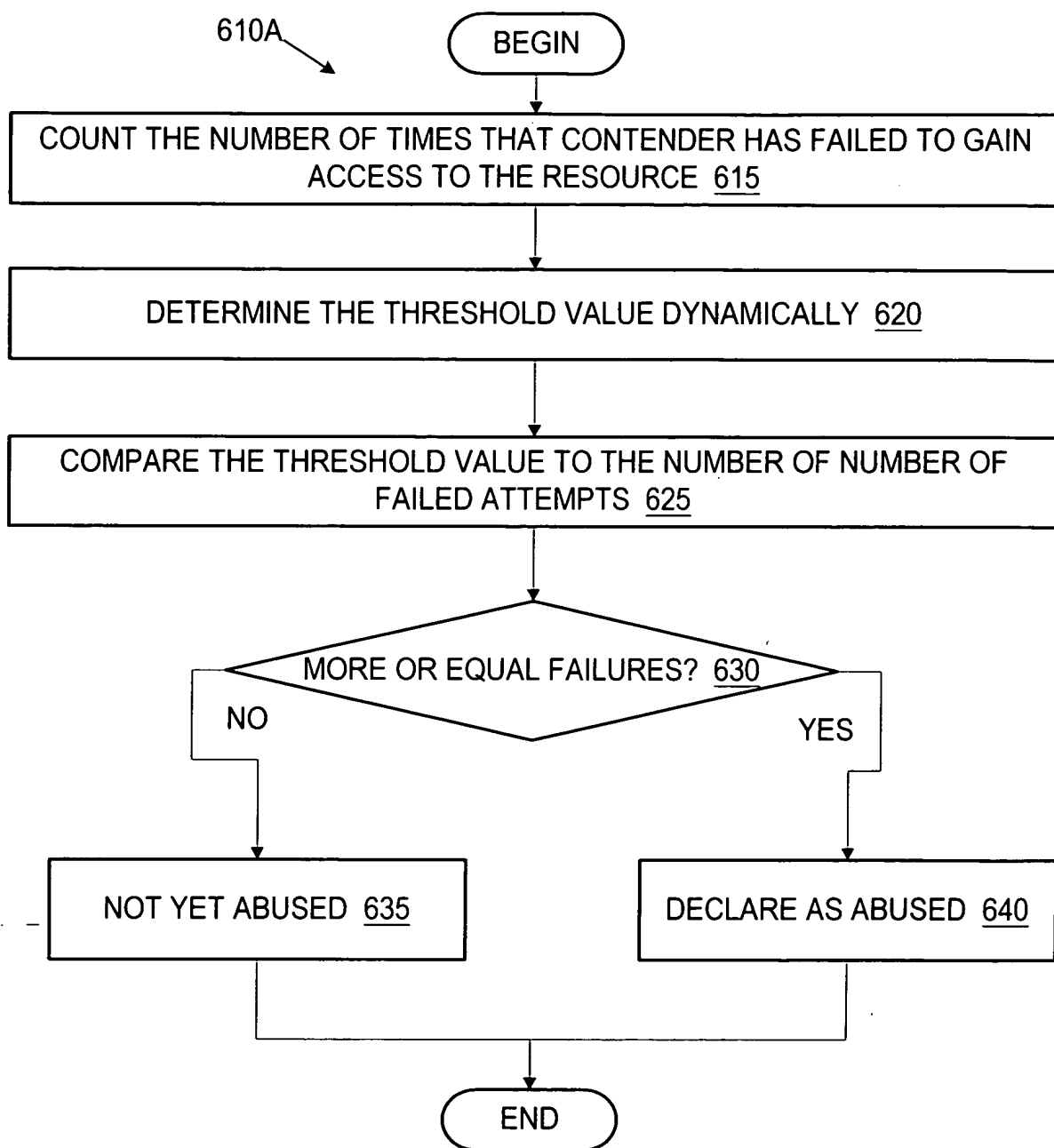


Fig. 6A

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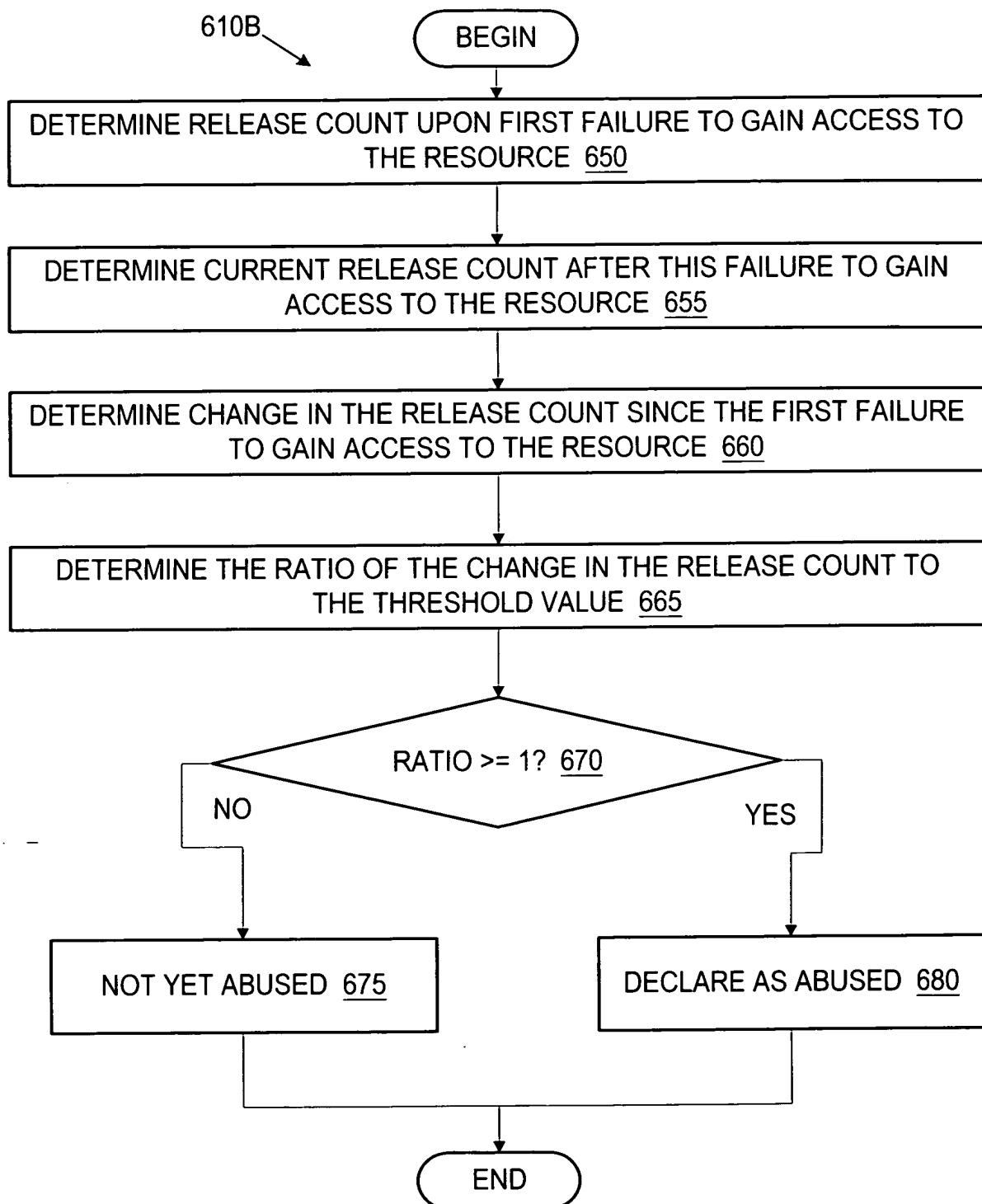


Fig. 6B

205010" 55524001

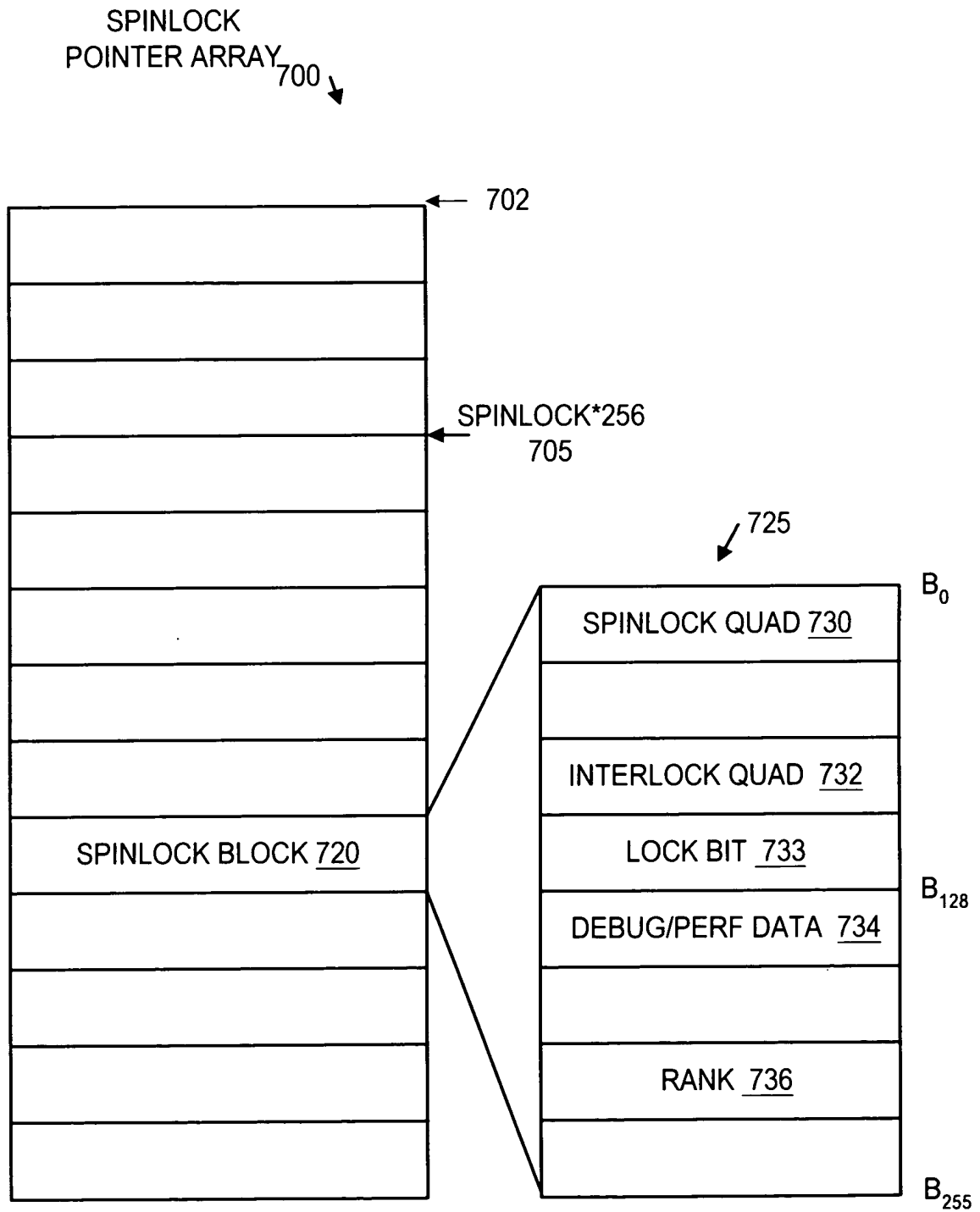


Fig. 7A

SPINLOCK DATA
BLOCK ARRAY 750

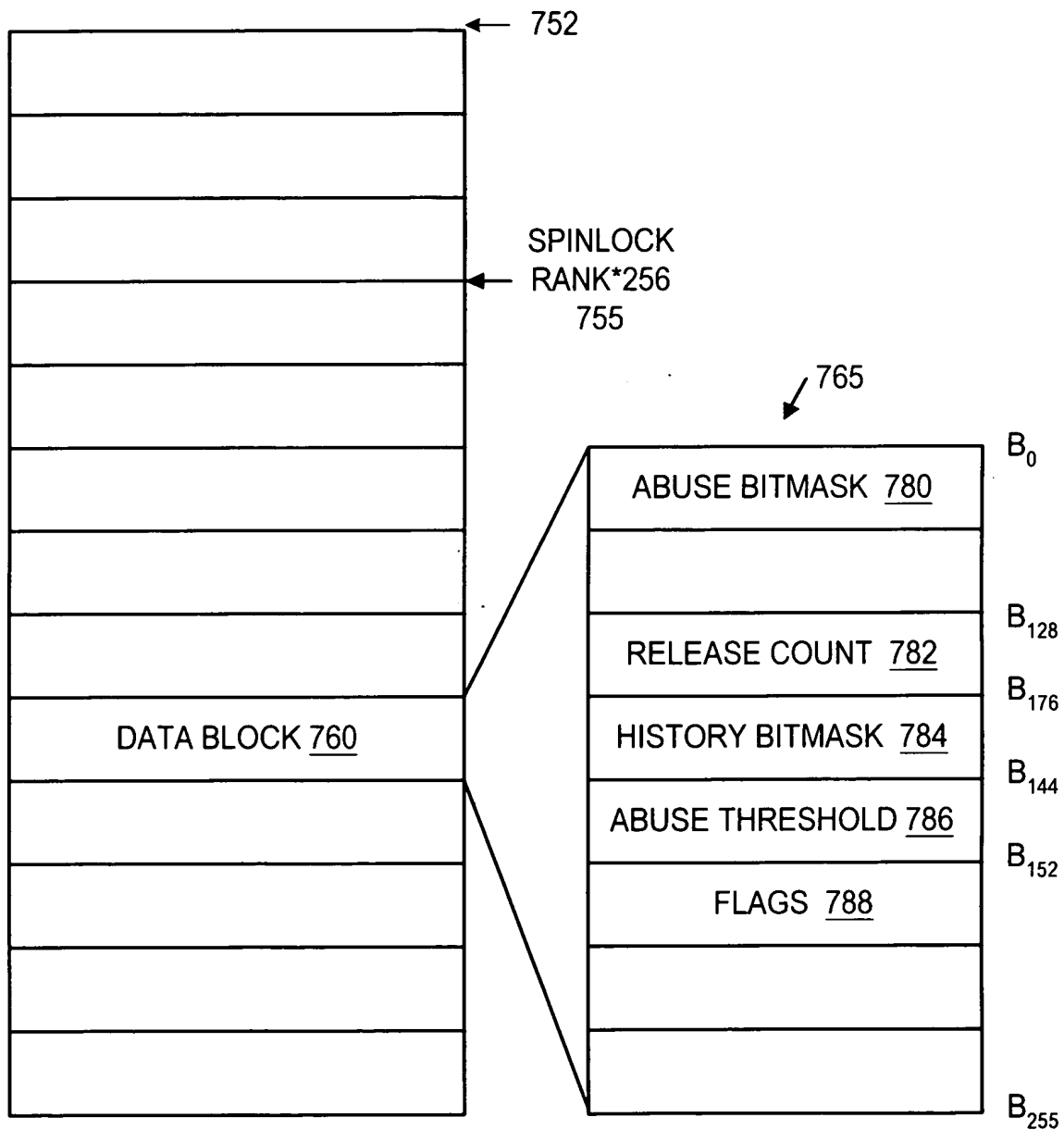


Fig. 7B

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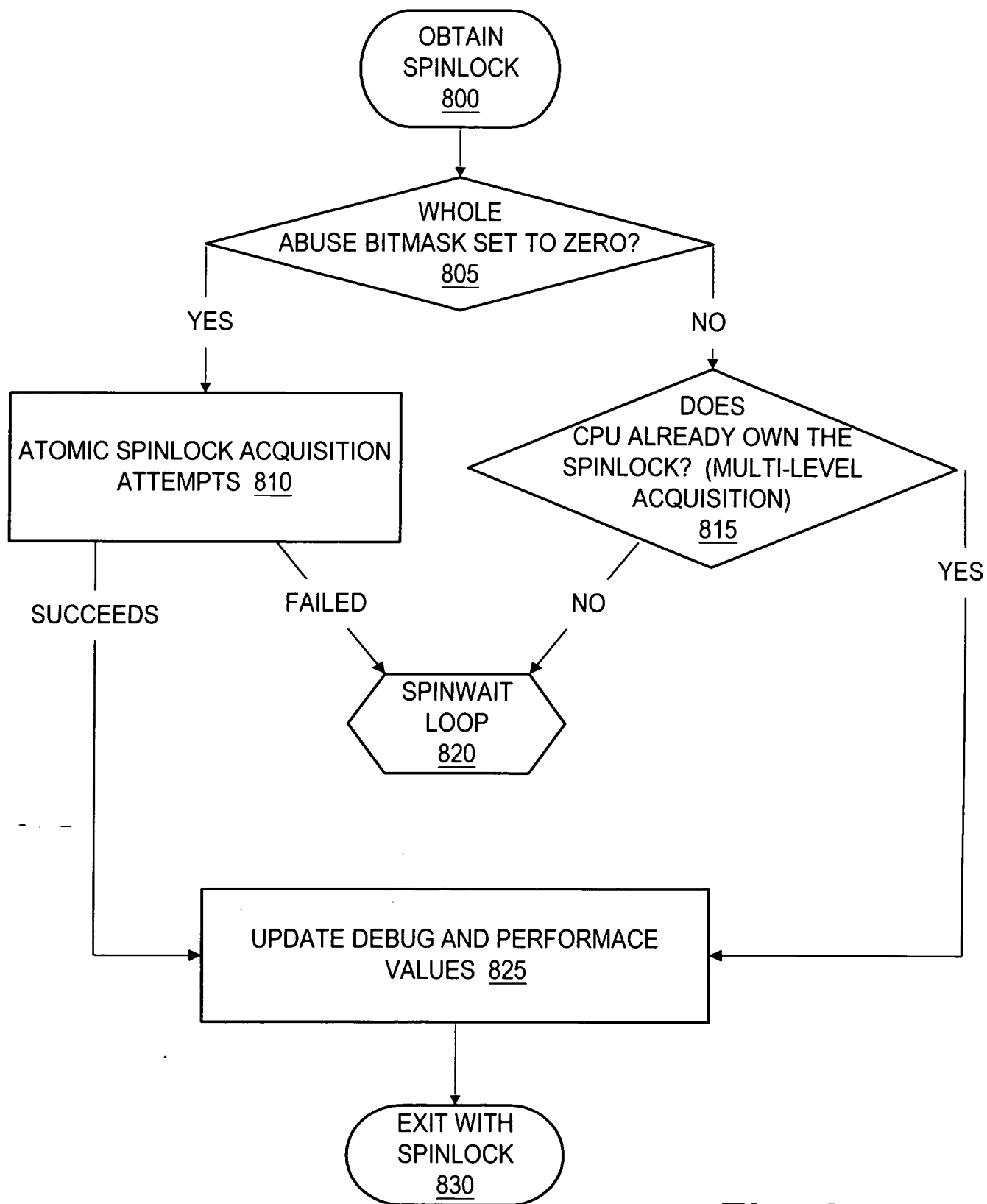


Fig. 8

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900 →

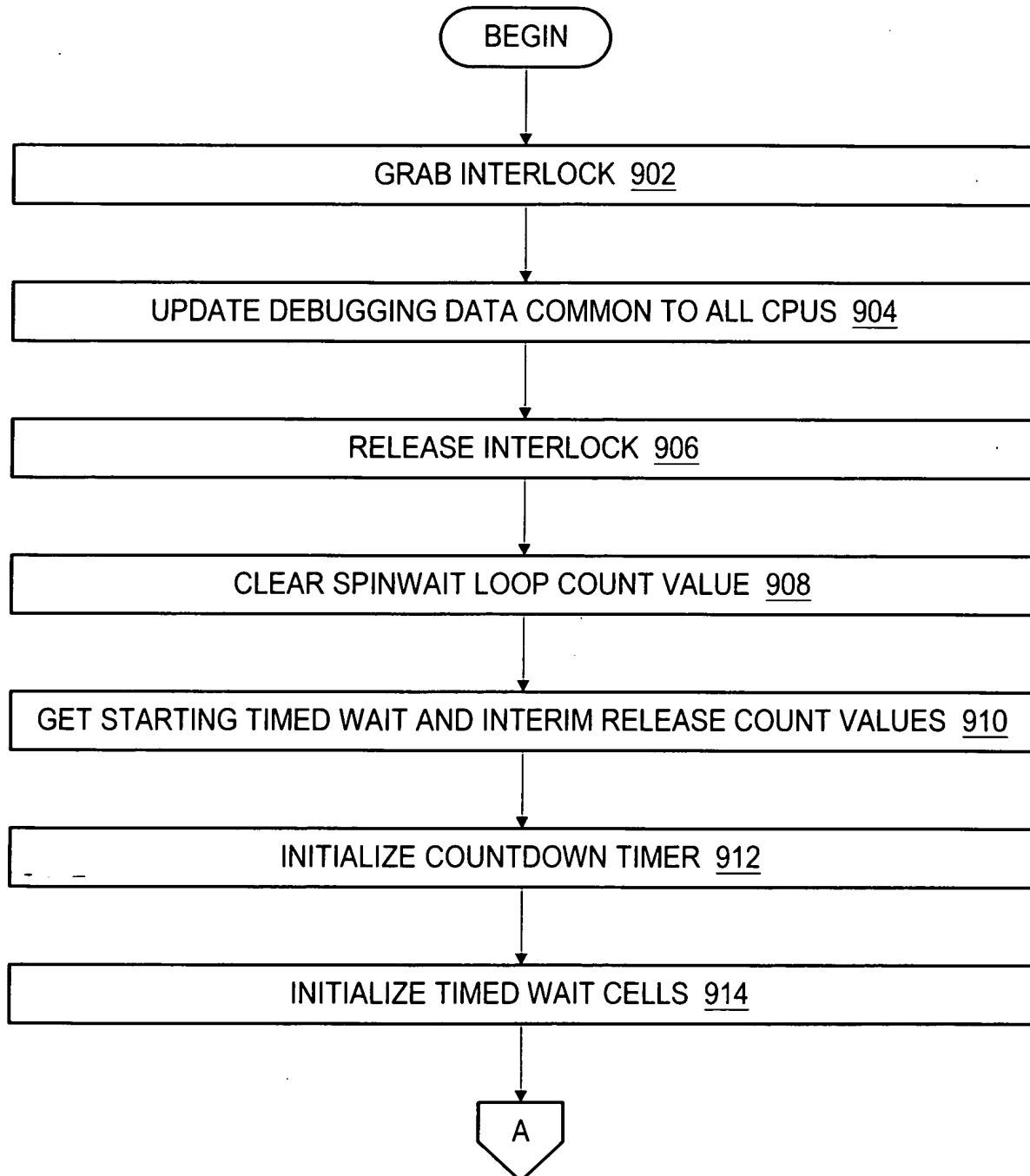


Fig. 9A

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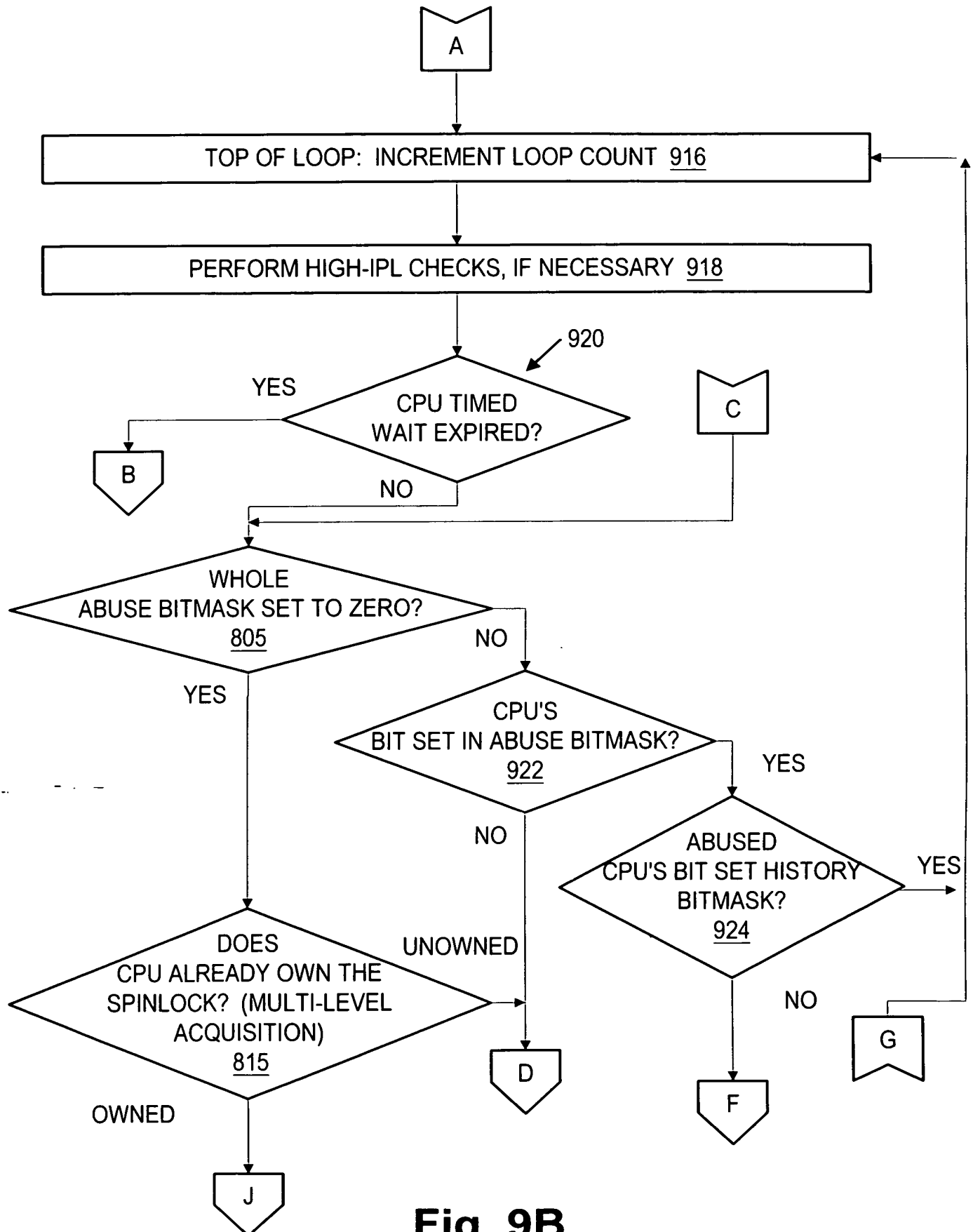


Fig. 9B

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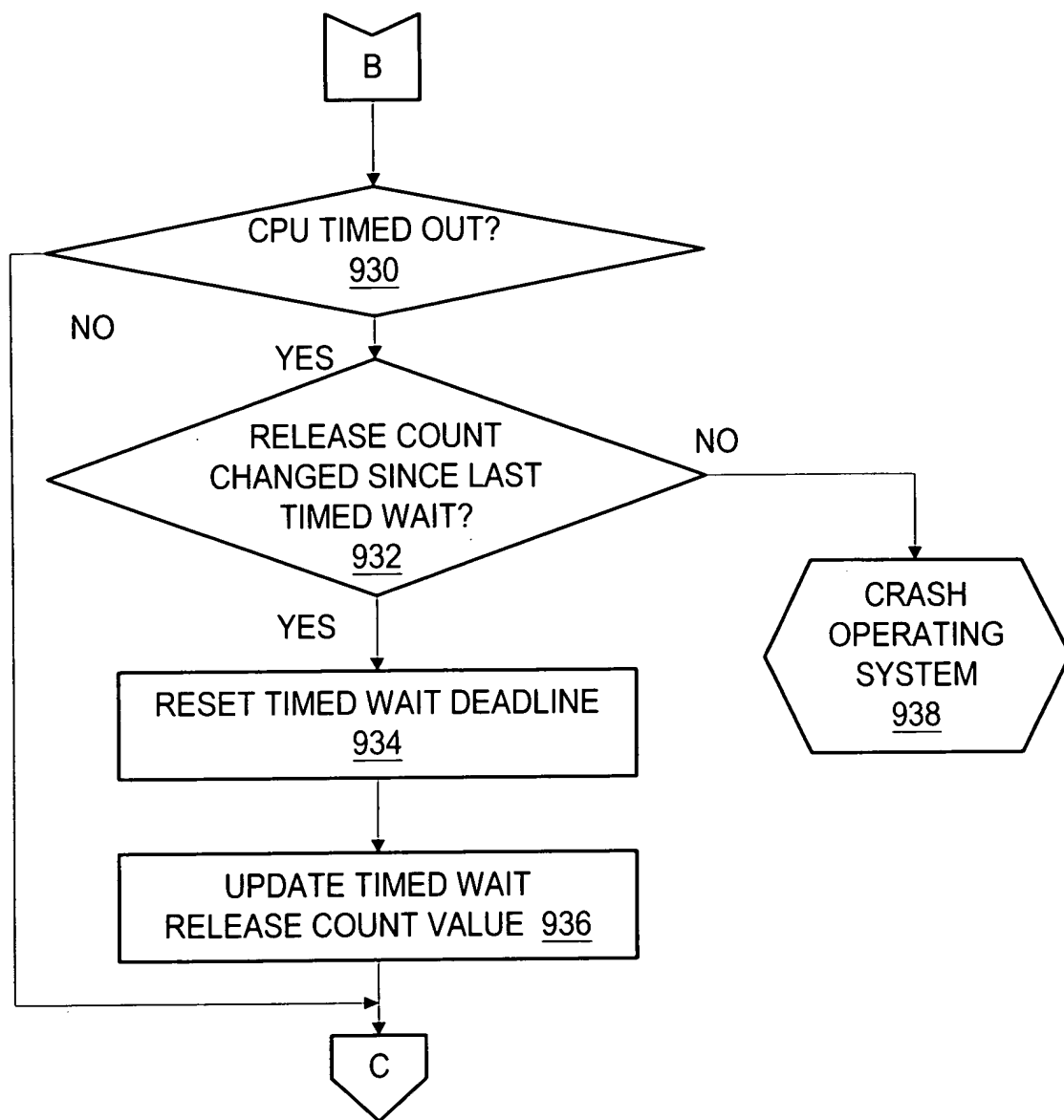


Fig. 9C

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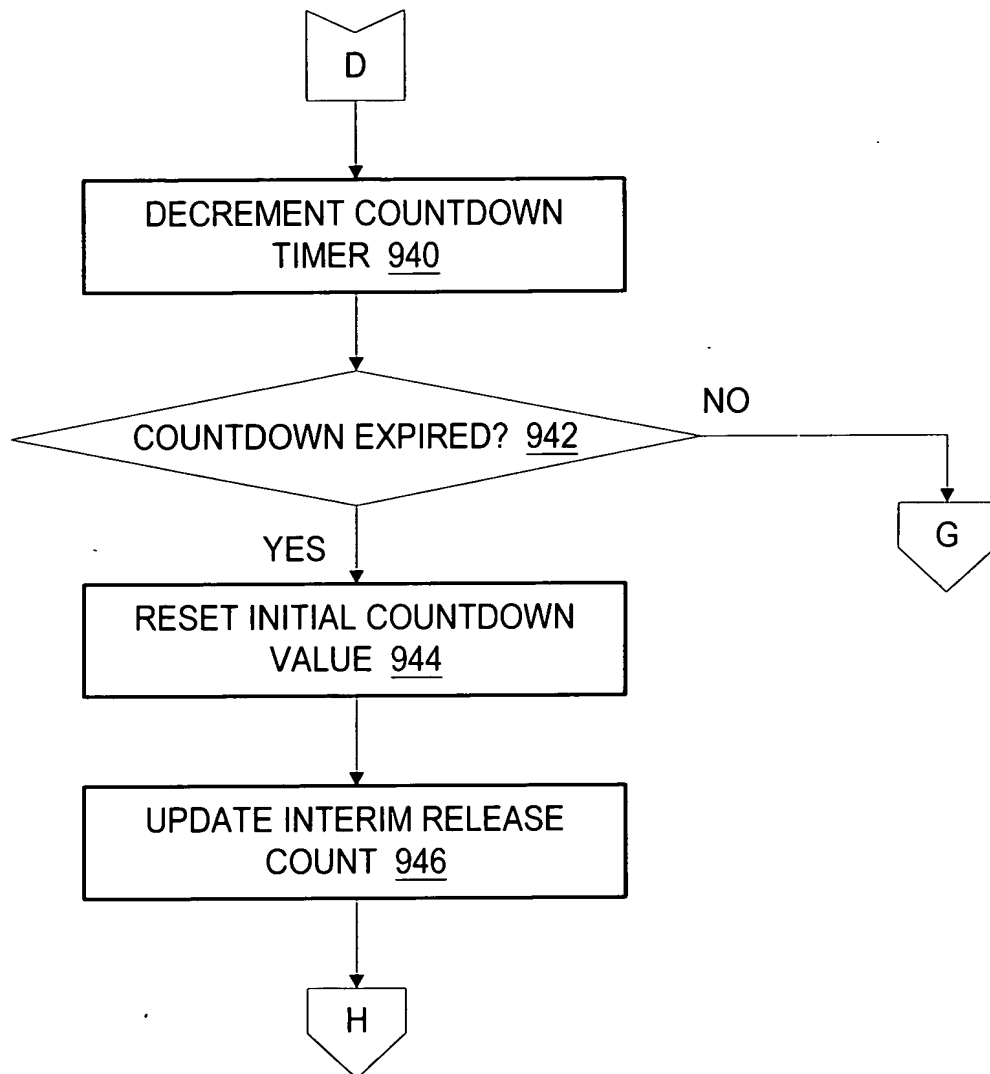


Fig. 9D

206070" 66624001

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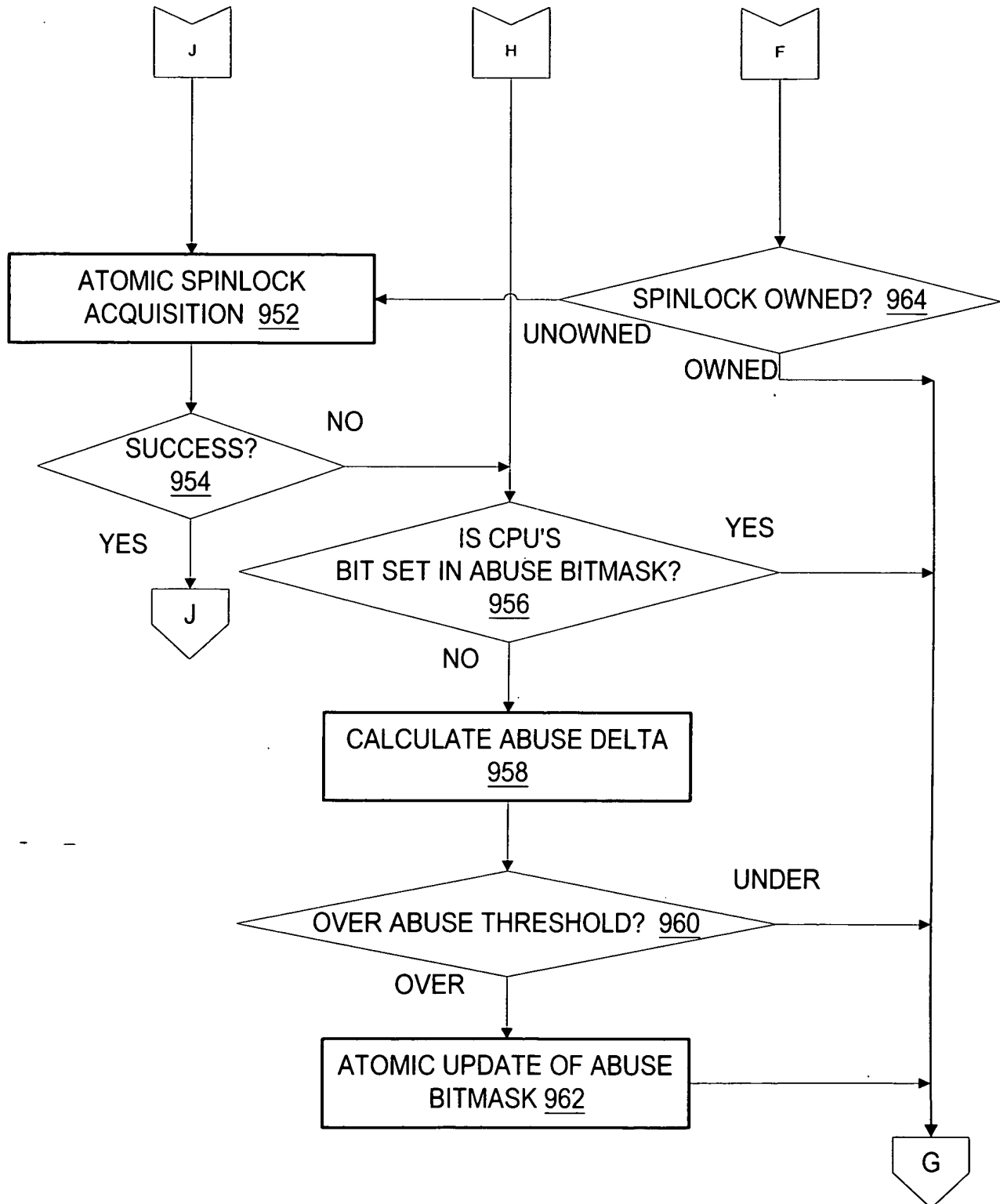
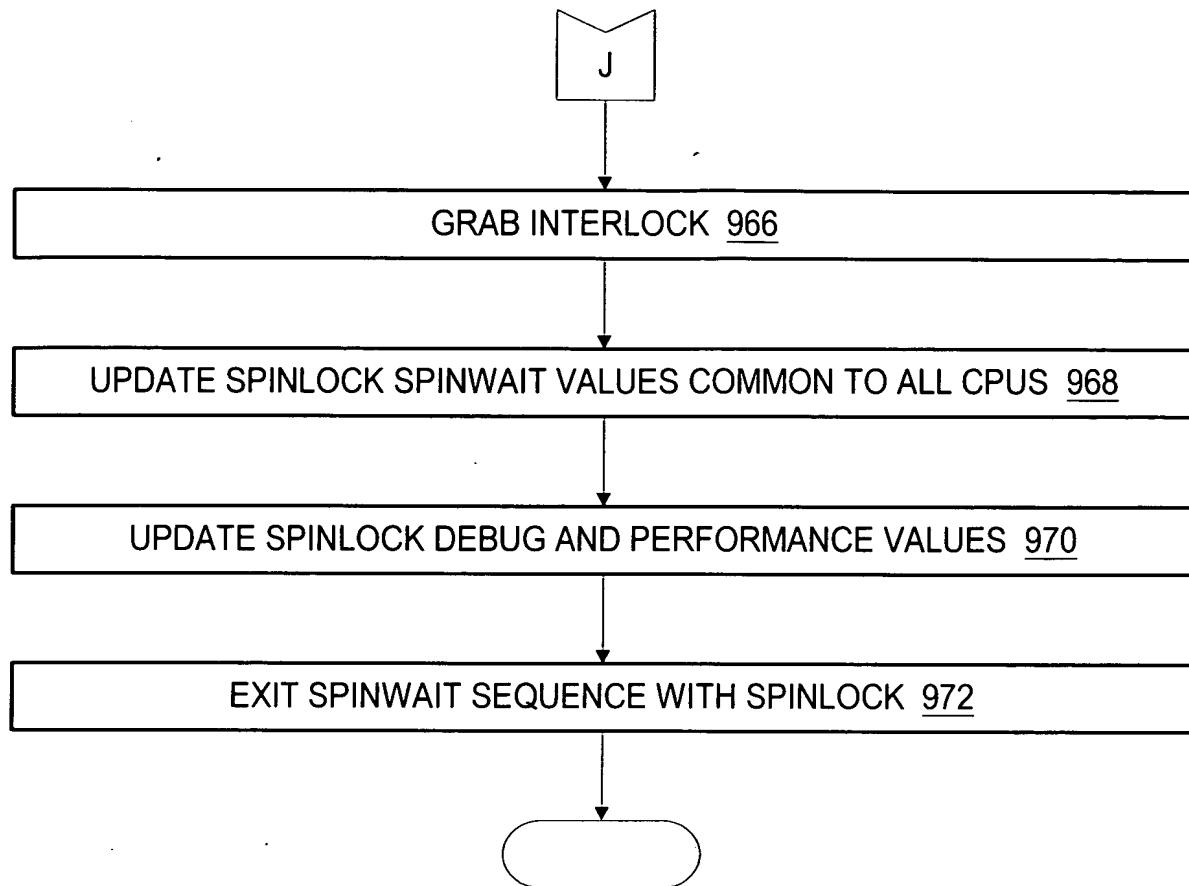


Fig. 9E

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1004299-010902

Fig. 9F

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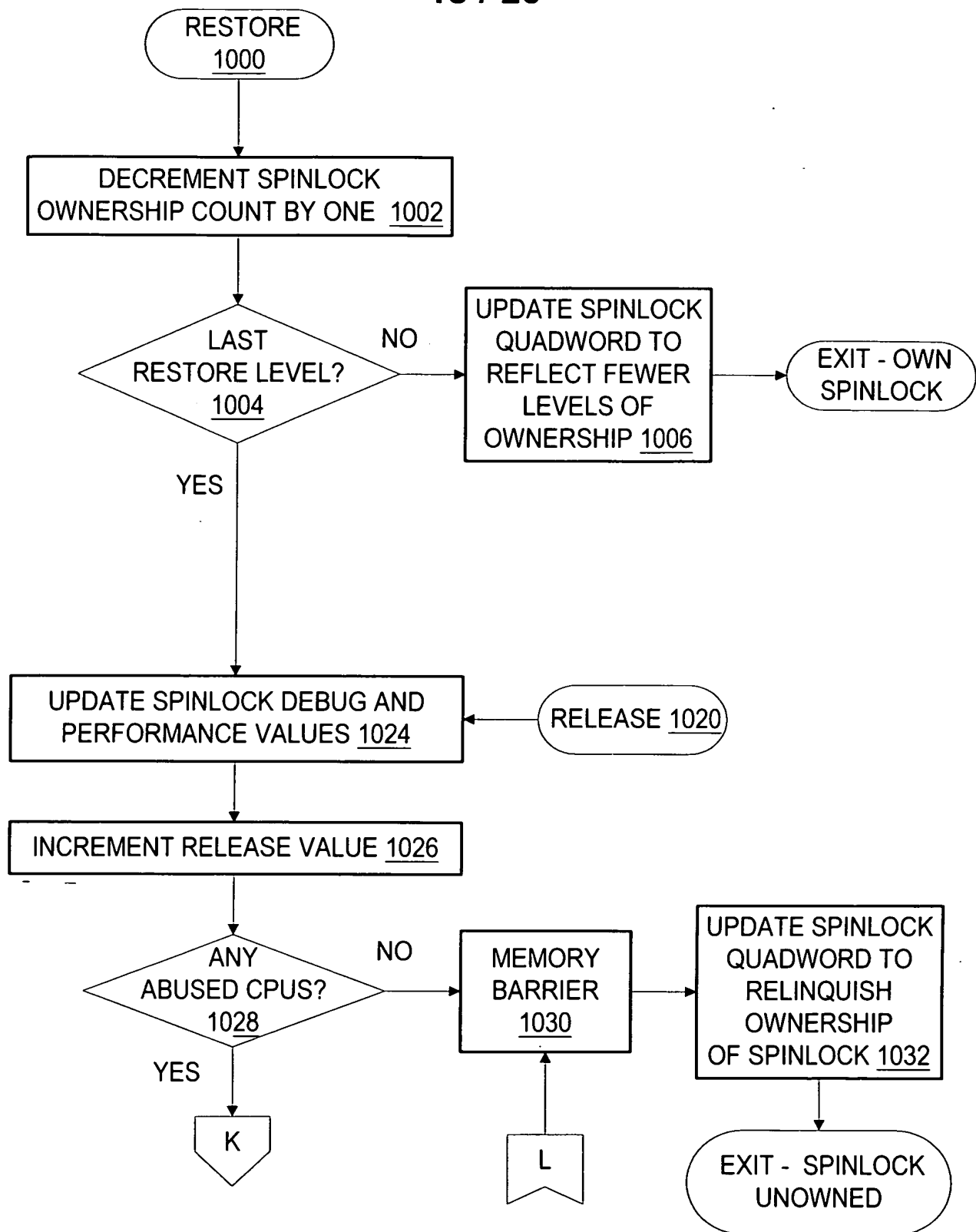


Fig. 10A

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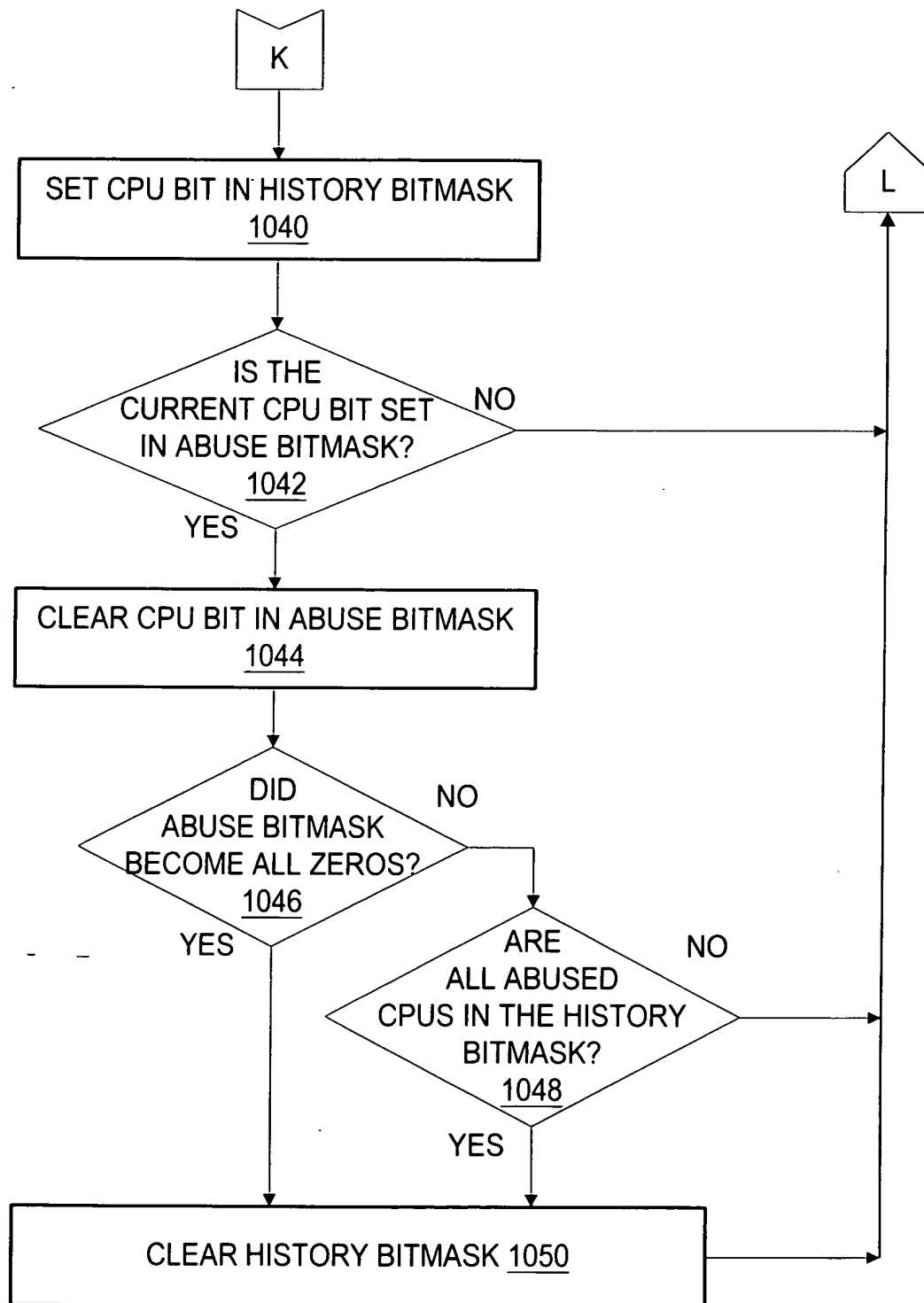


Fig. 10B

1100

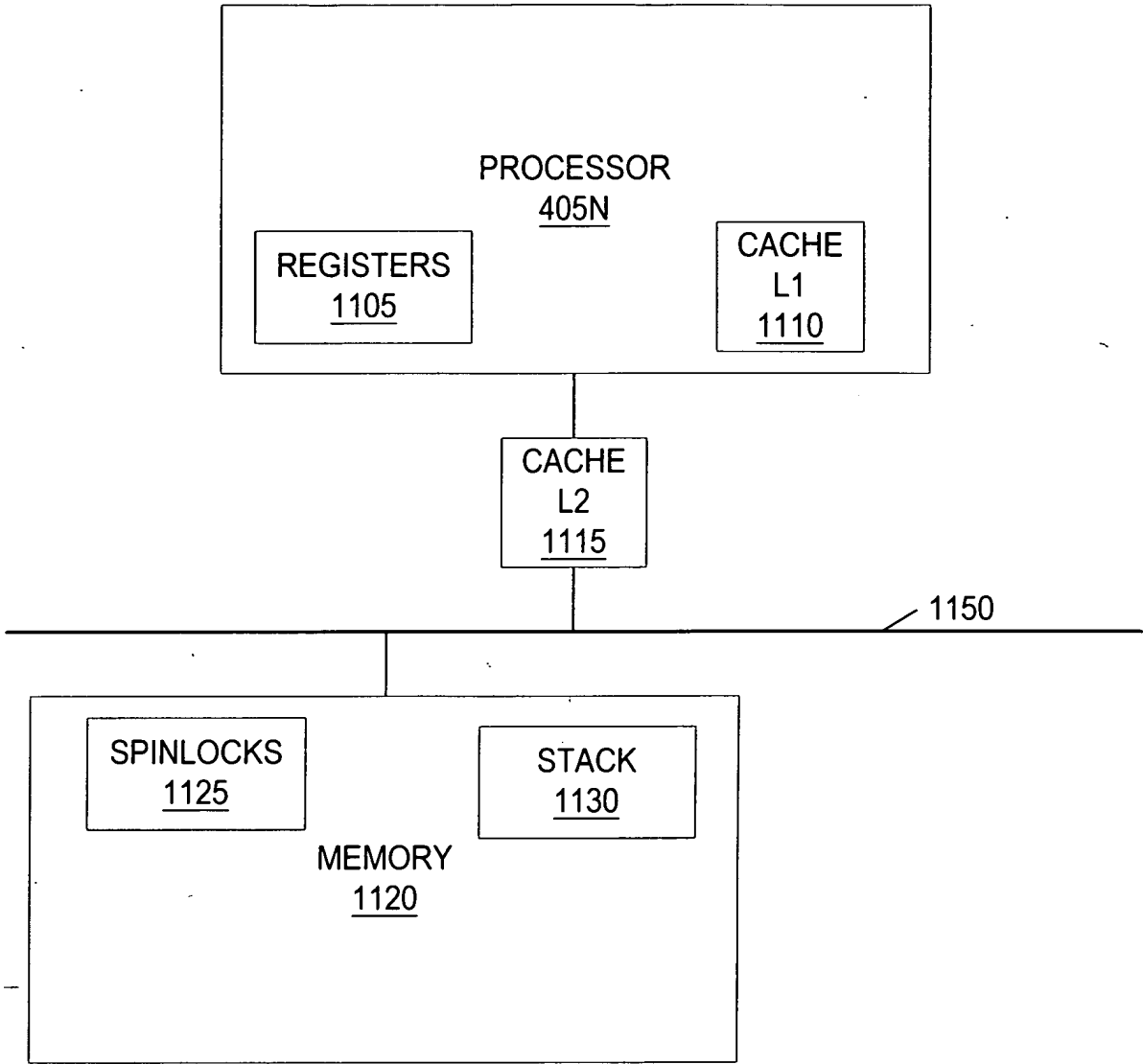


Fig. 11